



C. U. SHAH UNIVERSITY, Wadhwan City

Name of Programme: M.Tech

Name of Branch: VLSI & Embedded System Design

Semester: II

w. e. f. June 2014

Sr. No	Branch Code	Subject Code	Subject Name	Teaching Schemes (Hours)				Credits	Evaluation Schemes							
				Th	Tu	Pr	Total		Theory				Practical (Marks)			Total
									Sessional Exam		University Exam		Pr	TW	Pr	
									Marks	Hours	Marks	Hours				
1	05	5TE02RSM1	Research Methodology (RSM)	02	00	00	02	02	30	1.5	70	3.0	---	---	---	100
2	05	5TE02AVD1	Analog VLSI Design (AVD)	03	00	02	05	04	30	1.5	70	3.0	---	20	30	150
3	05	5TE02AES1	Advanced Embedded Systems (AES)	04	00	02	06	05	30	1.5	70	3.0	---	20	30	150
4	05	5TE02WTE1	Wireless Technology for Embedded Systems(WTE)	03	00	02	05	04	30	1.5	70	3.0	---	20	30	150
5	05	5TE02VDA1	VLSI Design Automation (VDA)	04	00	02	06	05	30	1.5	70	3.0	---	20	30	150
6	05	5TE02XXX1	Elective – II	03	00	02	05	04	30	1.5	70	3.0	---	20	30	150
			Total	19	00	10	29	24	180	---	420	---	---	100	150	850

ELECTIVE-I:

Subject Code	Subject Name
5TE02MSS1	Mixed Signal System Design (MSS)
5TE02FAA1	FPGA Architecture And Applications (FAA)
5TE02SDP1	System Design with Advanced Processors (SDP)
5TE02RKS1	RTOS Kernel & System Drivers (RKS)

Th: Theory, To: Tutorial/Seminar/Field work, Pr: Practical, TW: Term Work

Note: Theory _____ 1hr = 1 Credit,

Tutorial/Practical _____ 2hr = 1 Credit,